altering the identifier in the memory arrangement before at least one of erasing and programming the information.

3.   (Amended) The method according to Claim 1, wherein the altering step includes the substep of:

altering the identifier by at least one of erasing and programming.

4.   (Amended) The method according to Claim 1, further comprising the step of:

entering the identifier into a further area of the memory arrangement, the further area being at least one of erased and programmed only after at least one of erasing and programming of the area.

5.   (Amended) The method according to Claim 4, wherein the further area is to be at least one of erased and programmed last.

7.   (Amended) The method according to Claim 1, further comprising the step of:

altering the identifier by at least one of erasing and programming so that the identifier is unidentifiable.

9.   (Amended) The method according to Claim 1, further comprising the step of:

checking the identifier after at least one of (a) an interruption in at least one of erasing and programming and (b) at least one of erasing and programming the memory arrangement.

11.   (Amended) The method according to Claim 10, further comprising the steps of:

checking at least one of the identifier and the flag before at least one of erasing and programming; and

analyzing at least one of the identifier and the flag before at least one of erasing and programming.

12.   (Twice amended) A method of reprogramming information in a memory arrangement of a computer, comprising the step of:

selecting an identifier from the information entered into an area of the memory to be at least one of erased and programmed, the identifier identifying a correct at least one of erasing and programming of the memory arrangement.

14. (Twice amended) The method according to claim 12, further comprising the step of:

selecting the identifier from the information entered into a further area of the memory arrangement, the further area being at least one of erased and programmed only after at least one of erasing and programming of the area.

15. (Amended) The method according to Claim 14, wherein the further area is to be at least one of erased and programmed last.

16. (Twice amended) The method according to claim 12, further comprising the step of:

altering the selected identifier in the memory arrangement before at least one of erasing and programming the information.

17. (Amended) The method according to Claim 16, wherein the altering step includes the substep of:

altering the selected identifier by at least one of erasing and programming.

19. (Amended) The method according to Claim 12, further comprising the step of:

altering the identifier by at least one of erasing and programming so that the identifier is unidentifiable.

21. (Amended) The method according to Claim 12, further comprising the step of:

checking the identifier after at least one of (a) an interruption in at least one of erasing and programming and (b) at least one of erasing and programming the memory arrangement.

23. (Amended) The method according to Claim 22, further comprising the steps of:

checking at least one of the identifier and the flag before at least one of erasing and programming; and

analyzing at least one of the identifier and the flag before at least one of erasing and programming.

24. (Twice amended) A device for at least one of erasing and programming information in a memory arrangement of a computer, comprising: